From: AL250

AL250R
AL250G
AL250B
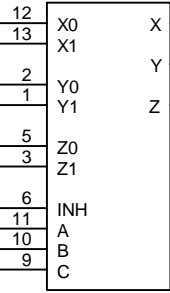
VCLKx2 — AL250GCLK
AL250GHS — AL250GHS
AL250GVS — AL250GVS

From: PC VGA

VGAR
VGAG
VGAB

PCGCLK — PCGCLK
PCHSYNC — PCHSYNC
PCVSYNC — PCVSYNC

U?

12	X0	X	14
13	X1		
2	Y0	Y	15
1	Y1	Z	4
5	Z0		
3	Z1		
6	INH		
11	A		
10	B		
9	C		

74HCT4053

U?

2	1A	1Y	4
3	1B		
5	2A	2Y	7
6	2B		
11	3A	3Y	9
10	3B		
14	4A	4Y	12
13	4B		
1	$\overline{A/B}$		
15	G		
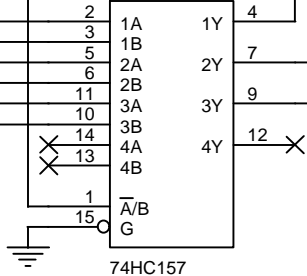
74HC157

To: A/D converter, LCD
Controller and Panel

LCDR — LCDR
LCDG — LCDG
LCDB — LCDB
LCDGCLK — LCDGCLK
LCDHSYNC — LCDHSYNC
LCDVSYNC — LCDVSYNC
SEL

Remarks:
1. Please refer to AL250EVB schematics for connection between decoder and the AL250.
2. LCD of resolution other than 640x480 may require a zoom engine.
3. SEL can use either H/W or S/W control. High: PC VGA, Low: TV.
4. Decoupling capacitors are needed for each CMOS IC.
5. Load resistance of 75 ohm is still needed for the AL250 RGB output.
6. PReliminary information for reference only.

Title	AL250 MUX for LCD Panel	
Size A	Document Number {Doc}	Rev {RevCode}
Date:	Tuesday, August 18, 1998	Sheet 1 of 1