VCK
/HSYNC
/VSYNC
VCK/2
/VCK/2
HREF

VCLKX2
VIDHS
VIDVS
VCLK
HREF

VDATA7
VDATA6
VDATA5
VDATA4
VDATA3
VDATA2
VDATA1
VDATA0

VDIN15
VDIN14
VDIN13
VDIN12
VDIN11
VDIN10
VDIN9
VDIN8

To the AL300/250/251 directly, or mux
with the output of the TV decoder.
Note that a mux is needed if both
MPEG and TV decoder are used.

U1

Pin	Signal		Signal	Pin
2	D1	Q1		19
3	D2	Q2		18
4	D3	Q3		17
5	D4	Q4		16
6	D5	Q5		15
7	D6	Q6		14
8	D7	Q7		13
9	D8	Q8		12

VDIN7
VDIN6
VDIN5
VDIN4
VDIN3
VDIN2
VDIN1
VDIN0

11 CLK
1 OC

74HC574

The CCIR 601 output of the ZiVA decoder uses an
8-bit interface, and needs to be converted to 16-bit
interface to work with the AL300/250/251.

Title	MPEG CCIR601 DEMUX			
Size	Document Number			Rev
A	MPEG DEMUX			A1
Date:	Wednesday, May 26, 1999	Sheet	1 of 1	